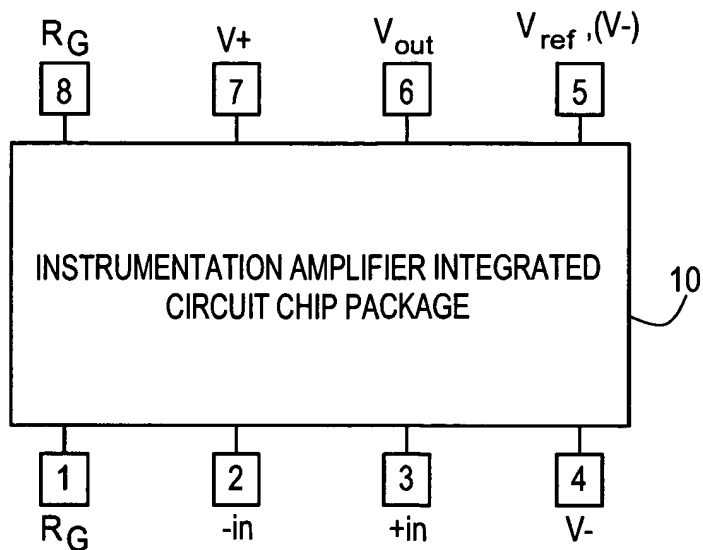


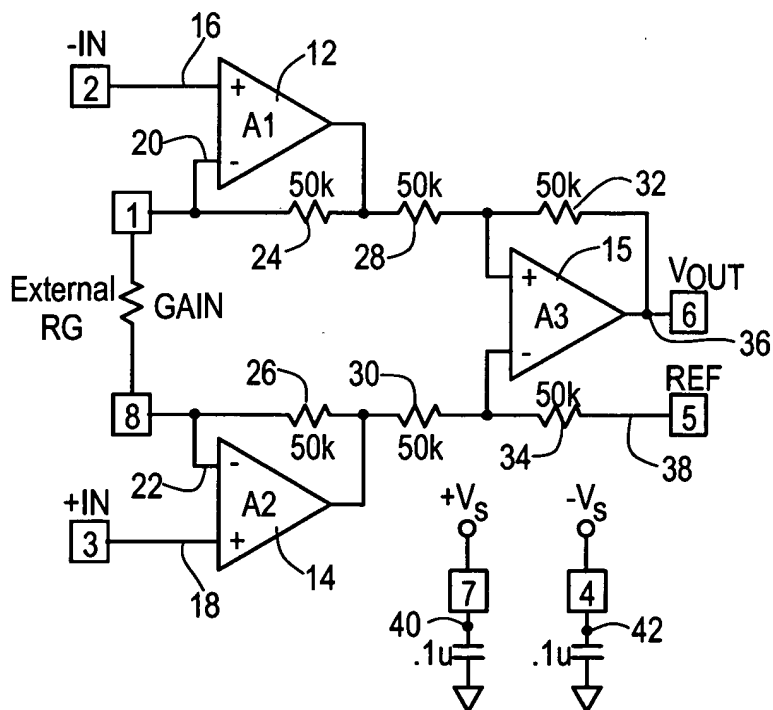


Applicants: Gerstenhaber et al.  
Title: REPATTERNED INTEGRATED  
CIRCUIT CHIP PACKAGE  
Serial No.: 09/679,658  
Docket No.: AD-230J  
Atty: David W. Poirier, Reg. No. 43,007  
REPLACEMENT SHEET 1 of 4

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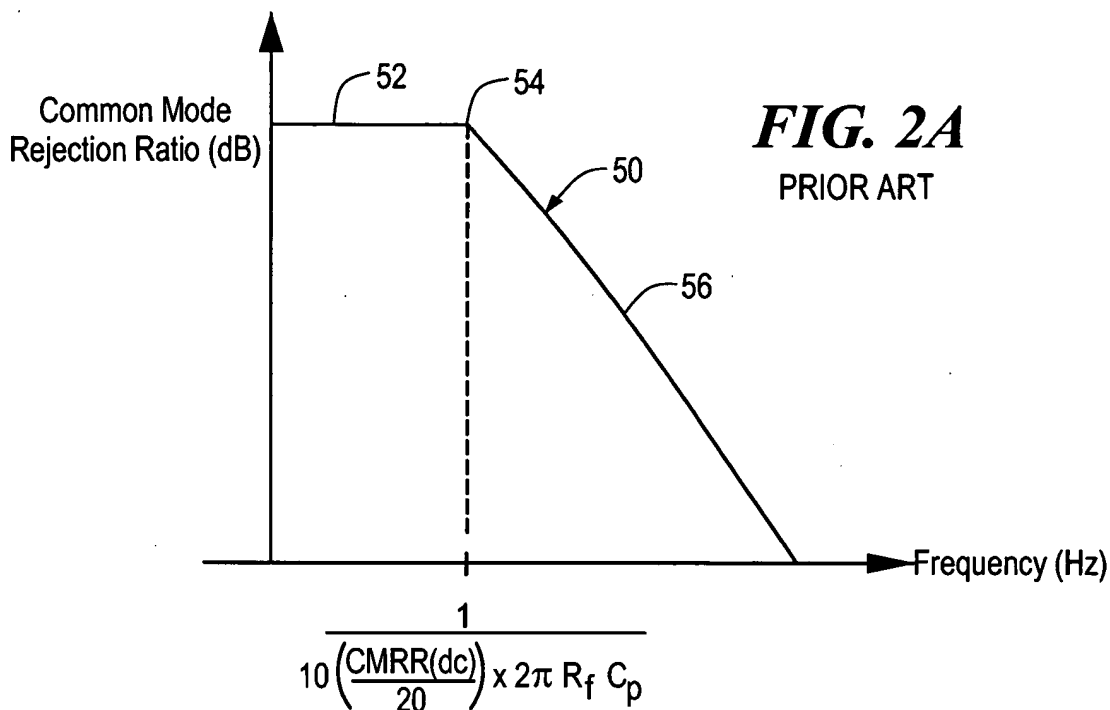
**FIG. 1**  
PRIOR ART



**FIG. 2**  
PRIOR ART



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COMMON MODE REJECTION RATIO VS. FREQUENCY

